Low Temperature Fabrication of BaTiO₃ Thick Films by Aerosol Deposition Method and Their Electric Properties

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The Aerosol Deposition Method (ADM) was employed in order to prepare $BaTiO_3$ thick films at low temperature for the applications to a decoupling capacitor. The optimum deposition conditions were investigated to grow $BaTiO_3$ thick films on copper metal substrates at room temperature. As a result, it was revealed that the dispersion state of ultrafine $BaTiO_3$ particles as a starting material was very important to improve surface morphology. It was succeeded in obtaining $BaTiO_3$ thick films with smooth surface. The as-deposited $BaTiO_3$ thick films were translucent and their crystal structure was assigned to cubic $BaTiO_3$ not tetragonal through the analysis of X-ray diffraction. The dielectric permittivity of the as-deposited $BaTiO_3$ thick films was around 1 % at 1 MHz regardless of the film thickness. To enhance the dielectric permittivity of $BaTiO_3$ thick films, the annealing treatment was applied. After annealing at 300°C for 2 hrs, the lattice parameters became closer to those of $BaTiO_3$ ceramics, and the dielectric permittivity also increased up to approximately 140. We could show the possibility for aerosol-deposited $BaTiO_4$, thick films to be applied to the embedded decoupling capacitor.

Key words: Aerosol Deposition Method, BaTiO₃, film, embedded decoupling capacitor

1. INTRODUCTION

Today's electronic devices, such as cellular phones, personal digital assistants (PDAs) and personal computers, continue to be subjected to the trend of added functionality with wireless interfaces and high operating speeds and of the miniaturization by high integration of electronic components. Especially, the number of passive components increases at very high rates. In the roadmap of the National Electronic Manufacturing Initiative (NEMI)¹, the surface mounting technology (SMT) of passive components, such as discrete parts, arrays, networks and integrated passive devices will be no longer satisfied due to the limited area left for the passive components. So, the embedded passive technology (EPT) should be developed to overcome this problems. Candidates of the present fabrication technologies for embedded passives are low temperature co-fired ceramics (LTCCs)^{2,3} and polymer-ceramic composites^{4,5}. LTCCs need still high temperature process at around 850 °C leading to low size accuracy due to shrinkage during firing, limitations on the use of materials caused by interdiffusion and reactions, difficulty in embedding of active devices and chip components. And, polymer-ceramic composites show degraded electric properties, low permittivity and low heat extraction compared with ceramics in spite of being able to fabricate lower than 200 °C and to embed active devices in the multilayer structure circuitry. Among the passive components, the decoupling capacitor with high dielectric permittivity are one of the most difficult components to fabricate as a embedded passive by means of the present fabrication technologies. For instance, relative permittivity of the embedded decoupling capacitor prepared by polymer composites is less than 100.6 Decoupling capacitors, which act as a charge reservoir, are required to suppress the power distribution noise which arises due to the fast switching speed of the circuits which result in sudden current demands, noise is generated in the system. However, traditional decoupling capacitors such as MLCCs (multilayer ceramic capacitors) are not effective at frequencies above 1 GHz. As advantages of EPT, decoupling capacitors can be placed directly underneath the high speed active components shortening distance between the passives and active components so that the parasitics associated with SMT passives can be reduced to result in better signal transmission and less cross talk, If BaTiO₃ thick films with high dielectric permittivity can be fabricated on Cu substrates below 300°C, all of the problems mentioned above are expected to be overcome. In this study, we propose a new novel technology to fabricate BaTiO, thick films as the decoupling capacitor by employing the aerosol deposition method (ADM)7-9.

2. EXPERIMENTAL

The ADM is based on shock loading solidification due to the impact of ultrafine ceramic particles accelerated by means of carrier gases and a nozzle. Figure 1 illustrates



Fig. 1. Schematic diagram illustrating the apparatus of the Aerosol Deposition Method.

the ADM apparatus. BaTiO₃ thick films were fabricated on glass or Cu substrates by the ADM. Cu substrates were mechanically polished to get a mirror-like plane using alumina powder of 1 µm in diameter. Fine BaTiO, particles (Sakai Chemical Industry Co., Ltd., BT03) with 0.3 µm in average diameter and purity of 99.9 % were used as starting materials. Before deposition, the BaTiO, particles were dried at 150°C for 1 day. The BaTiO, particles become a state of aerosol in the aerosol chamber by means of the vibration and mixing system. The particles are transported by He gases and accelerated through a nozzle. Films are grown at room temperature by the impact of the particles on substrates. The deposition conditions for BaTiO₃ thick films by the ADM were summarized in Table 1. The microstructure and crystallinity of the BaTiO, thick films were examined by a field emission-scanning electron microscope (FE-SEM) and X-ray diffractometer (XRD), respectively. In order to exmamine the dielectric properties of the ADM-derived BaTiO, thick films, the upper electrode was formed by Au using a DC sputter. Their dielectric properties were measured from 1 kHz to 1 MHz by an impedance analyzer (Agilent Tech. 4294A) and the coaxial microprobes (Cascade Microtech Japan, INC, DCP100). And temperature dependence of dielectric properties also measured using the temperature controllable probe station (WM365A-1, Measure Jig Co., Ltd.).



Fig. 2. Plane-view FE-SEM micrographs of BaTiO₃ films deposited on Cu substrates at room temperature by the ADM. (a) low magnification and (b) high magnification.

Table I.	Experimental	l conditions	of BaTiO ₃	thick films
by the a	erosol deposit	ion process.		

Powder	BaTiO,	
Substrate	Glass and Cu	
Carrier Gas	He	
Size of Nozzle Orifice	10×0.4 mm²	
Scanning Rate	1.25 mm/sec	
Working Pressure	1~1.5 kPa	
Consumption of Carrier Ga	4.5~9 L/min	
Distance between Substrate	10~35 mm	
and Nozzle		
Deposition Temperature	room temperature	
Deposition Time	10~60 min	
Deposition Area	5×5 mm²	
Vibration Speed	150~300 rpm	

3. RESULTS AND DISCUSSION

3.1 ADM-derived BaTiO₃ thick films prepared at room temperature

Figure 2 shows plane-view FE-SEM micrographs of the ADM-derived barium titanate thick films on Cu substrates using BaTiO, particles with 0.3 µm in average diameter at room temperature. The thickness of this sample was approximately 1 µm. In addition. BaTiO. thick films were prepared with various thickness from submiron to 5 µm. Deposition rate was in the range of 0.01-1 µm/min according to the experimental conditions. It could be confirmed that the barium titanate thick films are dense although their surfaces are somewhat rough. The surface roughness R is approximately $0.5 \mu m$. By the observation of FE-SEM at higher magnification, as shown in Fig. 2(b), it was clearly revealed that the films are very dense and the grain size is approximately 10-300 nm. However, TEM observation is required to determine more accurately. Figure 3 shows the XRD patterns of the BaTiO, thick films on Cu substrates at room temperature by the ADM and the raw material powder (BT03). Even though the films were prepared at room temperature, only perovskite-BaTiO, phase was formed. However, the peak splits indicating the tetragonal structure could not be observed in the XRD patterns of the ADM-derived BaTiO, thick films. Moreover, their XRD peaks were shifted to the lower angle than that of



Fig. 3. X-ray diffraction patterns of (a) raw material powder (BT03) and (b) BaTiO₃ films deposited on Cu substrates at room temperature by the ADM.



Fig. 4. Thickness dependence of permittivity of $BaTiO_3$ films deposited on Cu substrates at room temperature by ADM.

BaTiO, ceramics. And, the peak broadening was observed for the films, which is caused by the small crystallite size (15 nm in average). From the XRD results, it is considered that the ADM-derived BaTiO, thick films have cubic system and their lattice parameters are slightly expanded compared with those of BaTiO, bulk ceramics. This phenomenon is very similar to the size effect of BaTiO₂, but it has not been clearly understood yet and the further study is required to elucidate the phenomenon. At present, we consider that the growth of the films by the ADM is follows. When the BaTiO, thick films are grown by the ADM, the raw material BaTiO₃ particles are fractured to 10 nm-order sized particles by the impact so that their activation energy becomes much higher due to the high specific surface area, making the fractured particles to be bonded with the assist of the impact of the subsequent accelerated particles.

Their dielectric properties were investigated by an impedance analyzer. Figure 4 shows the thickness dependence of permittivity of BaTiO, thick films deposited on Cu substrates at room temperature by the ADM. The permittivity with various thickness ranging from submicron to several microns was not almost dependent on the film thickness. The average relative permittivity was around 70 and the dielectric loss tangent was 1 %. Although the dielectric property is good from the viewpoint of dielectric loss, dielectric permittivity is much lower than that of bulk ceramic, but it is higher than that of polymer composite by the use of BaTiO, particles. This is not the satisfied dielectric properties for the application to the embedded decoupling capacitors. In addition, the temperature dependence of dielectric permittivity and dielectric loss tangent of the ADMderived BaTiO, thick films prepared at room temperature. As shown in Fig. 5, the Curie point could not be observed up to 150°C, which phenomenon is considered to be occurred because the crystal structure of the ADM-derived BaTiO, thick films was determined to be cubic system as shown in XRD data. At present, we think that it is associated with the size effect. However, the reason has



Fig. 5. Temperature dependence of dielectric properties of $BaTiO_3$ films deposited on Cu substrates at room temperature by ADM.

not been understood clearly. A further study is necessary to clarify the mechanism and to improve the dielectric properties. Moreover, the hysteresis of the dielectric properties appeared as a function of the measurement temperature. This is considered to be from the annealing effect, which will be shown in the next section.

3.2 Post-Annealing Effects of ADM-derived $BaTiO_3$ thick films

In the previous section, we investigated the dielectric properties regarding the ADM-derived $BaTiO_3$ thick films prepared at room temperature. However, since the dielectric permittivity was too small for the applications, their post-annealing effects on the dielectric properties were also examined up to 300°C. First, the change in the crystal structure from the XRD analysis was examined according to the annealing temperature as shown in Fig. 6. As the annealing temperature increased, the peak shift to the lower angle deceased, indicating the lattice parameters became closer to that of $BaTiO_3$ ceramics, but the lattice expansion could not be fully recovered to the



Fig. 6. X-ray diffraction patterns of (a) raw material powder (BT03) and (b) as-deposited $BaTiO_3$ films, postannealed $BaTiO_3$ films at (c) 100°C, (d) 200°C, and (e) 300°C.



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Fig. 7. Post-annealing effects on the dielectric permittivity of ADM-deposited BaTiO, films on Cu substrates.

original lattice size through the post-annealing below 300°C. And, their crystallite size was not changed apparently by the annealing at such a low temperature. We think that the change in the lattice parameters is the interesting phenomenon because such low heat energy is generally not enough to change the lattice parameters. At present, we believe that H₂O molecules in the ADMderived BaTiO, thick films are related to the above results, being considered that the Ba2+, Ti4+ or O2- ions are hardly diffused for their lattices to be rearranged at 300°C. Although the starting material power was dried, H.O. OH-, CO₃²⁻ and other contamination were absorbed at the surface of the powder. It is considered that this kind of contaminations cannot be removed easily through the annealing at 100°C or 200°C. However, it can be considered that they are able to begin to be got rid of around 300°C so that their lattice parameters are changed.

Figure 7 shows the post-annealing effects on the dielectric permittivity of ADM-deposited BaTiO, films on Cu substrates. At 100°C annealing, there was no enhancement in the dielectric properties compared with as-deposited BaTiO, thick films. As the annealing temperature increased above 100°C, the dielectric permittivity increased. In particular, it is noticeable that the increase of the dielectric permittivity at 300°C was approximately 200 %. Figure 8 shows the frequency dependence of dielectric properties of the as-deposited and the post-annealed BaTiO₂ films at 300°C. For the both of samples, the frequency dependence was observed, which dependence was not so high. And, the dielectric permittivity and dielectric loss increased by annealing at 300°C. However, its dielectric loss is still less than 3 %. We think that this increase is not from the defects such as space charges but from the non-linearity of BaTiO₃ materials themselves.

Through the post-annealing below 300°C, relatively good dielectric properties could be obtained compared to that of polymer composites. Even though some further studies remained, we could show the possibility for the applications to the embedded decoupling capacitors.



Fig. 8. Comparison of frequency dependence of dielectric properties between (a) as-deposited and (b) post-annealed BaTiO, films deposited by the ADM.

4. CONCLUSIONS

The Aerosol Deposition Method (ADM) was employed in order to prepare BaTiO, thick films with high dielectric permittivity at low temperature for the applications of the embedded decoupling capacitors. It was succeeded to obtain BaTiO, thick films with smooth surface by the application of non-agglomerated BaTiO, particles. The as-deposited BaTiO, thick films were translucent and their crystal structure was assigned to cubic BaTiO, not tetragonal through the analysis of X-ray diffraction. 2. The dielectric permittivity of the as-deposited BaTiO, thick films was around 70 while the dielectric loss tangent of them was around 1 % at 1 MHz regardless of the film thickness.After annealing at 300°C for 2 hrs, the lattice parameters became closer to those of BaTiO, ceramics, and the dielectric permittivity also increased up to approximetaly 140. We could show the possibility for aerosol-deposited BaTiO, thick films to be applied to the embedded decoupling capacitors.

REFERENCE

[1] J. Dougherty, J. Galvagni, L. Marcanti, P. Sandborn, R. Charbonneau, and R. Sheffield, "The NEMI Roadmap Perspective on Integrated Passives" (2001).

[2] M. Lahti and V. Lantto, J. Eur. Ceram. Soc., 21, 1997-2000 (2001).

[3] A. A. Shapiro, M. L. Mccartney, and H. P. Lee, *Microelectr. J.*, 33, 443-447 (2002).

[4] D. K. Das-Gupta and K. Doughty, *Thin Solid Films*, **158**, 93-105 (1988).

[5] Y. Rao, S. Ogitani, P. Kohl, and P. Wong, *J. Appl. Polym. Sci.*, **83**, 1084 (2002).

[6] "Densizairyo", Kogyo Chosakai Publishing, Tokyo, September, (2002) pp. 24-77 [in Japanese].

[7] Akedo and M. Lebedev, *Recent Res. Devel. Mat. Sci.*,2, 51-77 (2001).

[8] J. Akedo and M. Lebedev, *Appl. Phys. Lett.*, 77, 1710-1712 (2000).

[9] J. Akedo, M. Ichiki, K. Kikuchi, and R. Maeda, *Sens.* & *Actuat. A-Phys.*, **69**, 106-112 (1998).

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